

Lab2 Part A  
Due Wednesday 9-27-00

Part A of this lab will familiarize you with the Xilinx Foundation software and basic FPGA usage. The software is windows NT based, so let me know if you can't log into the NT side of the machines.

You will complete chapters 1,2,and 6 of the tutorial in this lab. The tutorial can be found at the following location:

<http://www.cs.tamu.edu/course-info/cpsc483/common/xilinx/toc.html>

In order to get credit for this part of the lab, you will need to have chapters 2 and 6 checked off by me (the TA). This can be done during Lab time or whenever you catch me.

Do not delete the work you do in this lab, because it will be used in Part B (to be released later).

-Brian