Assignment 1

February 08, 2006

Turn-in Deadline: Wednesday (February 15, 2006)

Please turn-in the solutions by e-mail only.

1. Read the paper by Bill Moyer, “Low-power Design for Embedded Processors”, Proceedings of the IEEE, vol.89, (no.11), IEEE, November 2001, p.1576-1587. This is same paper we discussed in the class.

Write a review of the paper

○ A review should only have a brief summary of the paper. The majority of the review should critique the paper. What did you like? What didn't you like? What do you think are the interesting points? What are the weak arguments? What is clear/unclear?

○ About 1 page

2. Consider the IBM PowerPC 405LP as your embedded processor for case study. Can you relate what you have studied in the above paper versus what has been adopted in this processor? Also, in your one page report, comment about the dynamic power management in 405LP.

Hints: You will find recent literatures on 405LP from the papers by Brook Bishop et al. and other open source documents.